28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

FEATURES

Single, Dual and Triple models

- Operating temperature -55° to +125°C
- · Qualified to MIL-PRF-38534 Class H and K
- Radiation hardness assurance (RHA) to level R 100 kRad(Si)
- · Input voltage range 16 to 40 VDC
- · Transient protection 50 V for 50 ms
- Fully Isolated, magnetic feedback
- · Fixed high frequency switching, 600 kHz
- · Trim function or remote sense on single output models
- · Inhibit function
- · Synchronization function
- · Indefinite short circuit protection
- Up to 84% efficiency (up to 73% efficiency triple models)



VDC OUTPUT	MODELS											
		VDC OUTPUT										
1.5 ±5 +5 & ±12	SINGLE	DUAL	TRIPLE									
	1.5	±5	+5 & ±12									
2.5 ±12 +5 & ±15	2.5	±12	+5 & ±15									
3.3 ±15	3.3	±15										
5	5											
12	12											
15	15											

DESCRIPTION

The SMTR Series™ of 28 volt DC/DC converters offers up to 30 watts of output power from single, dual or triple output configurations. They operate over the full military temperature range of -55°C to +125°C with up to 84% efficiency (up to 73% efficiency triple models). SMTR converters are packaged in hermetically sealed metal enclosures, making them ideal for use in military, aerospace and other high reliability applications.

SCREENING

SMTR converters offer screening options to Space Prototype (O), Class H, or Class K. Radiation tolerant to Radiation Hardness Assurance (RHA) levels of "-" (O), "P," or "R," per MIL-STD-38534. Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA". See "Class H and K, MIL-PRF-38534 Screening" tables for more information.

CONVERTER DESIGN

The SMTR converters are constant frequency, pulse-width modulated switching regulators which use a quasi-square wave, single ended, forward converter design. Tight load regulation is maintained by using a wide bandwidth magnetic feedback and, on single output models, through use of remote sense. On dual output models, the positive output is independently regulated and the negative output is cross regulated through the use of tightly-coupled magnetics. The SMTR Triple Series DC/DC converter's design includes individual regulators on the auxiliary outputs which provide for no cross regulation error when a minimum 300 mA load is maintained on the main (+5) output.

SMTR converters have internal input filter that help reduce the need for external components in normal operation. For systems that require compliance with MIL-STD-461C's CE03 standard, Interpoint offers filter/transient suppression modules (including the SFMC-461 and SFCS28-461), which will result in compliance. For the lowest noise performance, connection of the case to input common is recommended. The connection can be hard-wired or AC coupled with a small ceramic bypass capacitor. Indefinite short circuit protection and overload protection are provided by a constant current-limit feature. This protective system senses current in the converter's secondary stage and limits it to approximately 125% of the maximum rated output current.

SYNCHRONIZATION

Synchronizing the converter with the system clock allows the designer to confine switching noise to clock transitions, minimizing interference and reducing the need for filtering. In sync mode, the converter will run at any frequency between 500 kHz and 675 kHz. The sync control operates with an active high at any duty cycle between 40% and 60%. The sync pin should be connected to input common pin when not in use.



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WIDE VOLTAGE RANGE

SMTR converters are designed to provide full power operation over a full 16 to 40 VDC voltage range. Operation below 16 volts, including MIL-STD-704D emergency power conditions is possible with derated power.

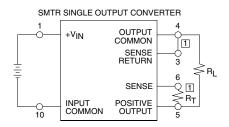
IMPROVED DYNAMIC RESPONSE

The SMTR Series feed-forward compensation system provides excellent dynamic response and noise rejection. Audio rejection is typically 50 dB (for Triple output models). The min. to max. step line transient response is typically less than 4%.

INHIBIT FUNCTION

SMTR converters provide an inhibit terminal that can be used to disable internal switching, resulting in no output and very low quiescent input current. The converter is inhibited when an active low (≤0.8V) is applied to the inhibit pin. The unit is enabled when the pin, which is internally connected to a pull-up resistor, is left unconnected or is connected to an open-collector gate. The open circuit output voltage associated with the inhibit pin is 9 to 11 VDC. In the inhibit mode, a maximum of 8 mA must be sunk from the inhibit pin.

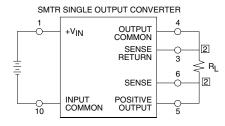
TRIM AND REMOTE SENSE (AVAILABLE ON SINGLE OUTPUT MODELS ONLY)



EXTERNAL TRIM CONNECTION

1 Make connections at converter.

FIGURE 1: TRIM CONNECTION^{1, 2, 3}



REMOTE SENSE CONNECTION

2 Make connections at load.

FIGURE 2: REMOTE SENSE^{2, 3}

Trim Formulas

Vout = desired output voltage; Rt = trim resistor

3.3V: Rt = 1300 * Vout - 4304

1.2475 5V: Rt = 1300 * Vout - 6512 1.2475 12V: Rt = 1300 * Vout - 15631

1.2475 15V: Rt = 1300 * Vout - 19498 1.2475 Notes for Remote Sense and Trim

- When trimming output voltage and/or remote sensing, the total output voltage increase must be less than 0.6 volts at the converters pins to maintain specified performance.
- If neither voltage trim nor remote sense will be used, connect pin 3 to pin 4 and pin 5 to pin 6 or the output voltage will increase by 1.2 volts.
- 3. CAUTION: The converter will be permanently damaged if the positive remote sense (pin 6) is shorted to ground. Damage may also result if the output common or positive output is disconnected from the load with the remote sense leads connected to the load.

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OPERATING CONDITIONS AND CHARACTERISTICS

Input Voltage Range

- 16 to 40 VDC continuous
- 50 V for 50 msec transient

Output Power

· Up to 30 watts depending on model

Lead Soldering Temperature (10 sec per pin)

• 300°C

Storage Temperature Range (Case)

-65°C to +135°C

Case Operating Temperature (Tc)

- · -55°C to +125°C full power
- · -55°C to +135°C absolute

Derating Output Power/Current

Linearly from 100% at 125°C to 0% at 135°C

Output Voltage Temperature Coefficient

- 100 ppm/°C typical single and dual outputs
- · 200 ppm/°C main, 300 ppm/°C aux triple output

Input to Output Capacitance

• 50 pF typ (100 pF typ triple outputs)

Current Limit

125% of full load typical

Isolation

- 100 megohm minimum at 500 VDC
- · Any pin to case, except case pin

Audio Rejection

• 40 dB typ (50 dB typ, triple output)

Conversion Frequency

- Free run 550 min, 600 typ, 650 max kHz
- External sync 500 to 675 kHz

SYNC AND INHIBIT

Synd

- Input frequency 500 to 675 kHz
- · Duty cycle 40% min, 60% max
- · Active low 0.8 V max
- Active high 4.5 V min, 5 V max
- · Referenced to input common
- · If not used, connect to input common

Inhibit

- · Active low (output disabled)
 - Voltage ≤0.8 V
 - ► Inhibit pin source current 8.0 mA max
 - ► Referenced to input common
- Active high (output enabled)
 - ► Open collector or unconnected
 - If not used, leave unconnected
 - ► Inhibit pin voltage, 9 to 11 V

MECHANICAL AND ENVIRONMENTAL

Size (maximum)

- · Single and dual output
 - ► Non-flanged: 2.125 x 1.115 x 0.400 inches (53.98 x 28.58 x 10.16 mm) See case H2 for dimensions.
 - ► Flanged: 2.910 x 1.115 x 0.400 inches (73.79 x 28.58 x 10.16 mm) See case K3 for dimensions.
- Triple output
 - Non-flanged: 1.950 x 1.350 x 0.405 inches
 (49.53 x 34.29 x 10.29 mm) See case F1 for dimensions.
 - Flanged: 2.720 x 1.350 x 0.405 inches (69.09 x 34.29 x 10.29 mm) See case J1 for dimensions.

Weight (maximum)

- · Single and dual non-flanged 50 grams, flanged 52 grams
- · Triple non-flanged 58 grams, flanged 62 grams

Screening

Space Prototype (O), Class H, or Class K are radiation tolerant to Radiation Hardness Assurance (RHA) levels of "-" (O), "P" or "R", per MIL-STD-38534. Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation hardness Assurance level of MIL-PRF- 38534, which is defined as "no RHA".

See "Class H and K, MIL-PRF-38534 Screening" tables for more information, page 21. Available configurations are: OO, HP, KP, HR, and KR

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		PIN OUT	
Pin	Single Output	Dual Output	Triple Output
1	Positive Input	Positive Input	Positive Input
2	Inhibit	Inhibit	Main (+5) Output
3	Sense Return	Positive Output	Output Common
4 Output Common		Output Common	Neg. Aux. Output
5	Positive Output	Negative Output	Pos. Aux. Output
6	Positive Sense	Case Ground	Case Ground
7	Case Ground	Case Ground	Case Ground
8 Case Ground		Case Ground	Inhibit
9 Sync		Sync	Sync
10	Input Common	Input Common	Input Common

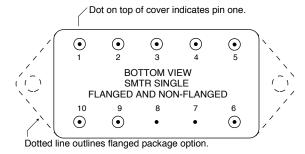


FIGURE 3: PIN OUT SINGLE OUTPUT MODELS See cases H2 and K3 for dimensions.

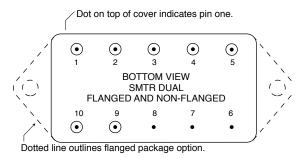


FIGURE 4: PIN OUT DUAL OUTPUT MODELS See cases H2 and K3 for dimensions.

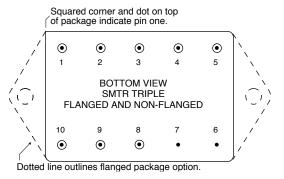
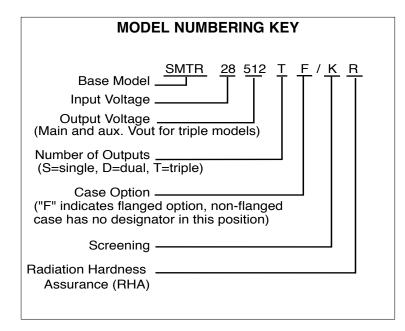


FIGURE 5: PIN OUT SMTR TRIPLE See cases F1 and J1 for dimensions.

PINS NOT IN USE								
Inhibit	Leave unconnected							
Sync In	Connect to Input Common							
Sense Lines	Must be connected to appropriate outputs							

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SMD N	UMBERS
STANDARD MICROCIRCUIT DRAWING (SMD)	SMTR SIMILAR PART
IN PROCESS	SMTR281R5S/KR
IN PROCESS	SMTR282R5S/KR
5962R0150102KXC	SMTR283R3S/KR
5962R9306802KXC	SMTR2805S/KR
5962R9306902KXC	SMTR2812S/KR
5962R9307002KXC	SMTR2815S/KR
5962R9320502KXC	SMTR2805D/KR
5962R9307102KXC	SMTR2812D/KR
5962R9307202KXC	SMTR2815D/KR
IN PROCESS	SMTR28512T/KR
5962R9307402KXC	SMTR28515T/KR

To indicate the flanged case option change the "X" to "Z" In the SMD number. The SMD number shown is for Class K screening, non-flanged, and Radiation Hardness Assurance (RHA) level R. See the SMD for the numbers for other screening and radiation levels. For exact specifications for an SMD product, refer to the SMD drawing. Call your Interpoint representative for status on the SMTR SMD releases which are "in process." SMDs can be downloaded from:

http://www.dscc.dla.mil/programs/smcr

MODEL SELECTION ENTER ONE SELECTION FROM EACH CATEGORY									
SMTR28	·			/					
BASE MODEL	V _{OUT} VALUE ¹	NUMBER OF OUTPUTS ²	CASE OPTION ³	SCREENING ⁴	RHA ⁵				
	1R5, 2R5, 3R3, 05, 12, 15	s	(BLANK FOR NON-FLANGED)	0	0				
	05, 12, 15	D	F (FLANGED)	н	Р				
	512, 515	Т		K	R				

Notes:

- 1. VOUT Value: An R indicates a decimal point. 1R5 is 1.5 volts out. The values os 1R5, 2R5, and 3R3 are only available in single output models. The 512 and 515 triple output converters are +5 volt main and ±12 or ±15 volt auxiliaries.
- 2. S is a single output, D is a dual output, and T is a triple output
- 3. The case option is left blank for the standard, non-flanged, case. For the flanged case, use an F in the case option.
- 4. A screening level of O is a Space Prototype and is only used with RHA O.
- 5. Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA. RHA O is only available with Screening level O. See page 21 for more information.

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Electrical Characteristics: -55 to +125° C T_C , 28 VDC Vin, 100% load, no irraditaion, unless otherwise specified.

SINGLE OU	JTPUT MODELS	SM	ITR281F	R5S	SM	1TR285F	R2S	SN	/ITR283F	R3S	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	25°C	1.485	1.50	1.515	2.475	2.50	2.525	3.267	3.30	3.333	
		1.455	_	1.545	2.425	_	2.575	3.201	_	3.399	VDC
OUTPUT CURRENT	VIN = 16 TO 40 VDC	0	_	8.0	0	_	8.0	0	_	6.06	Α
OUTPUT POWER ²	VIN = 16 TO 40 VDC	_	-	12	_	-	20	_	_	20	W
OUTPUT RIPPLE	25°C	_	20	50	_	25	65	_	20	40	mV p-p
10 кHz то 2 MHz		_	-	50	_	-	65	_	_	50	
LINE REGULATION	VIN = 16 TO 40 VDC	_	_	10	_	_	10	_	_	10	mV
LOAD REGULATION	NO LOAD TO FULL	_	_	10	_	_	10	_	_	10	mV
INPUT VOLTAGE	NO LOAD TO FULL CONTINUOUS	16	28	40	16	28	40	16	28	40	VDC
	TRANSIENT 50 ms ¹	_	_	50	_	_	50	_	_	50	V
INPUT CURRENT	25°C, NO LOAD	_	30	60	_	30	60	_	30	75	
	NO LOAD	_	_	90	_	_	100	_	_	75	mA
	INHIBITED	_	_	8	_	_	8	_	_	8]
INPUT RIPPLE CURRENT ³	10 кHz - 10 MHz	_	_	50	_	_	50	_	_	50	mA p-p
EFFICIENCY	25°C	57	61	_	68	71	_	70	76	_	
		55	_	_	66	_	_	66	_	_	%
LOAD FAULT ⁷	SHORT CIRCUIT POWER DISSIPATION	_	_	13	_	_	13	_	_	12	W
	RECOVERY 1	_	_	6	_	_	6	_	_	6	ms
STEP LOAD RESPONSE ⁵	50% - 100% - 50% LOAD TRANSIENT	_	_	±125	_	_	±150	_	_	±250	mV pk
	RECOVERY 1, 4	_	_	200	_	_	200	_	_	200	μs
STEP LINE	TRANSIENT	_	_	±300	_	_	±300	_	_	±300	mV pk
RESPONSE ^{1, 5} 16 - 40 -16 VDC	RECOVERY ⁴	_	_	300	_	_	300	_	_	300	μs
START-UP ⁶	DELAY	_		5	_	_	5	_		5	ms
	OVERSHOOT FULL LOAD ¹	_	_	30	_	_	30	_	_	50	mV pk
	OVERSHOOT NO LOAD ¹	_	_	75	_	_	125	_	_	150	IIIV PIC
CAPACITIVE LOAD ¹	25°C , NO EFFECT ON DC PERFORMANCE	_	_	1000	_	_	1000	_	_	300	μF

Notes (All temperatures in tables and notes refer to case temperature, $\rm T_{C}.)$

^{1.} Guaranteed by design, not tested.
2. Operation is limited below 16 V (see Figure 22).
3. Tested with 6800 pF ceramic bypass capacitors connected externally from input common to case.

^{4.} Recovery time is measured from application of the transient to the point at which Vout is within 1% of final value.

^{5.} Transient transition time > 10 μ s.

^{6.} Tested on release from inhibit.

^{7.} Short circuit protection not guaranteed above 125°C case temperature.

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Electrical Characteristics: -55 to +125 $^{\circ}$ C T_C, 28 VDC Vin, 100 $^{\circ}$ load, no irraditaion, unless otherwise specified.

SINGLE OU	ITPUT MODELS	SM	1TR28280)5S	S	MTR2812	2S	s	MTR2815	SS.	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	25°C	4.95	5.00	5.05	11.88	12.00	12.12	14.85	15.00	15.15	
		4.85	_	5.15	11.64	_	12.36	14.70	_	15.30	VDC
OUTPUT CURRENT	VIN = 16 TO 40 VDC	0	_	5.0	0	_	2.5	0	_	2.0	Α
OUTPUT POWER ²	VIN = 16 TO 40 VDC	_	-	25	_	-	30	_	_	30	W
OUTPUT RIPPLE	25°C	-	20	50	_	15	40	_	15	40	
10 кHz то 2 MHz		_	_	90	_	_	90	_	_	90	mV p-p
LINE REGULATION	VIN = 16 TO 40 VDC	_	_	50	_	_	50	_	_	50	mV
LOAD REGULATION	NO LOAD TO FULL	_	_	50	_	_	50	_	_	50	mV
INPUT VOLTAGE	NO LOAD TO FULL CONTINUOUS	16	28	40	16	28	40	16	28	40	VDC
	TRANSIENT 50 ms ¹	_	_	50	_	_	50	_	_	50	V
INPUT CURRENT	25°C, NO LOAD	_	35	75	_	35	75	_	35	75	
	NO LOAD	_	_	75	_	_	75	_	_	75	mA
	INHIBITED	_	_	8	_	_	8	_	_	8	1 11,7
INPUT RIPPLE CURRENT ³	10 кHz - 10 MHz	_	_	50	_	_	50	_	_	50	mA p-p
EFFICIENCY	25°C	74	78	_	78	83	_	79	84	_	
		71	_	_	75	_	_	76	_	_	%
LOAD FAULT ⁷	SHORT CIRCUIT POWER DISSIPATION	_	_	12	_	_	12	_	_	12	w
	RECOVERY 1	_	_	5	_	_	5	_	_	5	ms
STEP LOAD RESPONSE ⁵	50% - 100% - 50% LOAD TRANSIENT	_	_	±300	_	_	±400	_	_	±500	mV pk
	RECOVERY 1, 4	_	_	200	_	_	200	_	_	200	μs
STEP LINE	TRANSIENT	_	_	±300	_	_	±500	_	_	±600	mV pk
RESPONSE ^{1, 5} 16 - 40 -16 VDC	RECOVERY ⁴	_	_	300	_	_	300	_	_	300	μs
START-UP ⁶	DELAY	-	_	5	_	_	5	_	_	5	ms
	OVERSHOOT FULL LOAD ¹	1	_	50	_	_	120	_	_	150	mV pk
	OVERSHOOT NO LOAD1	_	_	250	_	_	600	_	_	750	IIIV PIK
CAPACITIVE LOAD ¹	25°C , NO EFFECT ON DC PERFORMANCE	_	_	300	_	_	3000	_	_	3000	μF

Notes

(All temperatures in tables and notes refer to case temperature, T_{C} .)

- 1. Guaranteed by design, not tested.
- 2. Operation is limited below 16 V (see Figure 22).
- Tested with 6800 pF ceramic bypass capacitors connected externally from input common to case.
- 4. Recovery time is measured from application of the transient to the point at which
- Vout is within 1% of final value. 5. Transient transition time > 10 μ s.
- 6. Tested on release from inhibit.
- 7. Short circuit protection not guaranteed above 125°C case temperature.

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Electrical Characteristics: -55 to +125° C T_C , 28 VDC Vin, 100% load, no irraditaion, unless otherwise specified.

DUAL OUTPUT MODELS			MTR280	5D	s	MTR2812	2D	SI	MTR2815	5D	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	25°C +Vout	4.95	5.00	5.05	11.88	12.00	12.12	14.85	15.00	15.15	
	25°C -Vout	-4.93	5.00	-5.08	-11.82	-12.00	-12.18	-14.77	-15.00	-15.23	
	-55 то +125°С +Vоит	4.85	_	5.15	11.64	_	12.36	14.55	_	15.45	VDC
	-55 то +125°С -Vouт	-4.83	_	-5.18	-11.58	_	-12.42	-14.47	-	-15.53	
OUTPUT CURRENT ^{2, 3}	EITHER OUTPUT Vin = 16 to 40 VDC	0	2.50	4.50	0	1.25	2.25	0	1.00	1.80	Α
OUTPUT POWER 3, 4	VIN = 16 TO 40 VDC	_	_	25	_	_	30	_	_	30	W
OUTPUT RIPPLE	25°C	_	20	40	_	30	80	_	25	80	mV p-p
±Vout 10 κHz to 2 MHz		_	_	90	_	_	120	_	_	120	
LINE REGULATION	+Vout	_	_	50	_	_	50	_	_	50	
Vin = 16 to 40 VDC	-Vout	_	_	100	_	_	150	_	_	180	mV
LOAD REGULATION	+Vout	_	_	50	_	_	50	_	_	50	
BALANCED, NO LOAD TO FULL	-Vout	_	_	100	_	_	150	_	_	180	mV
CROSS REGULATION	SEE NOTE 5	_	_	12	_	_	8.3	_	_	8	%
EFFECT ON -V _{OUT} ¹	SEE NOTE 6	_		6	_	_	6	_	_	6	
INPUT VOLTAGE	NO LOAD TO FULL CONTINUOUS	16	28	40	16	28	40	16	28	40	VDC
	TRANSIENT 50 ms ¹	_	_	50	_	_	50	_	_	50	V
INPUT CURRENT	25°C, NO LOAD	_	35	75	_	50	75	_	50	75	
	NO LOAD	_	_	75	_	_	75	_	_	75	mA
	INHIBITED	_		8	_	_	8	_	_	8	
INPUT RIPPLE CURRENT ⁷	10 кHz - 10 MHz	_	_	50	_	_	50	_	_	50	mA p-p
EFFICIENCY	25°C	74	76	_	77	80	_	78	81	_	
		74	_	_	77	_	_	78	_	_	%
LOAD FAULT ⁸ ±Vout	SHORT CIRCUIT POWER DISSIPATION	_	_	10	_	_	10	_	_	10	W
	RECOVERY 1, 9	_	_	5.0	_	_	5.0	_	_	5.0	ms
STEP LOAD RESPONSE ¹⁰	50% - 100% - 50% LOAD TRANSIENT	_	_	±300	_	_	±300	_	_	±400	mV pk
±V _{OUT} Balanced Load	RECOVERY 9	_	_	200	_	_	200	_	_	200	μs
STEP LINE	TRANSIENT	_	_	±400	_	_	±400	_	_	±500	mV pk
RESPONSE ^{1,10} ±V _{OUT,} 16 - 40 -16 VDC	RECOVERY 9	_	_	300	_	_	300	_	_	300	μs
START-UP	DELAY 7	_	_	5	_	_	5	_	_	5	ms
±V _{OUT}	OVERSHOOT FULL LOAD ¹	_	_	180	_	_	120	_	_	150	mV pk
	OVERSHOOT NO LOAD ¹	_	_	250	_	_	600	_	_	750	ти рк
CAPACITIVE LOAD ¹	25°C, NO EFFECT ON DC PERFORMANCE	_	_	500	_	_	500	_	_	500	μF

Notes: See page 9

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Notes SMTR Dual Output Models

(All temperatures in tables and notes refer to case temperature, T_C.)

- 1. Guaranteed by design, not tested
- 2. The specified max current is available from either output.
- Up to 90% of the total output current/power is available from either output providing the positive output is carrying at least 10% of the total output power.
- 4. Operation is limited below 16 V (see Figure 22).
- 5. Effect on the negative output under the following conditions: +Pout 20% to 80%; -Pout 80% to 20%
- 6. Effect on the negative output under the following conditions: +Pout 50%; -Pout 10% to 50%
- Tested with 6800 pF ceramic bypass capacitors connected externally from input common to case.
- Indefinite short circuit protection not guaranteed above 125°C case temperature.
- 9. Recovery time is measured from application of the transient to point at which Vout is within 1% of final value.
- 10. Transition time \geq 10 μ s.

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Electrical Characteristics: -55 to +125 $^{\circ}$ C T_C, 28 VDC Vin, 100 $^{\circ}$ load, no irraditaion, unless otherwise specified.

TRIPLE OUTPUT MODEL -	- SMRT28512T	;	5 (MAIN	l)	+12 (+AUX)			-12 (-AUX)			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	25°С, +Vоuт	4.95	5.00	5.05	11.82	12.00	12.18	11.82	12.00	12.18	
	+Vout	4.85	5.00	5.15	11.58	12.00	12.42	11.58	12.00	12.42	VDC
OUTPUT CURRENT ^{2, 3}		0.30	_	4.00	_	_	±0.625	_	_	±0.625	Α
OUTPUT POWER 2, 3		_	_	20	_	_	7.5	_	_	7.5	
	TOTAL ALL OUTPUTS	_	_	30	_	_	_	_	_	_	
OUTPUT RIPPLE	10 кHz - 2 MHz	_	_	180	_	_	±120	_	_	±120	mV p-p
LINE REGULATION 4	Vin 16 to 40 VDC	_	_	20	_	_	±75	_	_	±75	mV
LOAD REGULATION 3, 4		_	_	50	_	_	±75	_	_	±75	mV
INPUT VOLTAGE	CONTINUOUS	16	28	40	_	_	_	_	_	_	VDC
	TRANSIENT 50 ms ¹	_	_	50	_	_	_	_	_	_	V
INPUT CURRENT	NO LOAD	_	_	110	_	_	_	_	_	_	mA
	INHIBITED	_	_	6	_	_	_	_	_	_	1117 (
INPUT RIPPLE CURRENT 3	10 kHz - 10 MHz	_	_	80	_	_	_	_	_	_	mA p-p
EFFICIENCY	25°C	71	73	_	_	_	_	_	_	_	%
		69	_	_	_	_	_	_	_	_	/6
LOAD FAULT 5	POWER DISSIPATION	_	_	14	_	_	_	_	_	_	W
ALL OUTPUTS SHORTED	RECOVERY EACH OUTPUT ¹	_	_	6	_	_	_	_	_	_	ms
STEP LOAD RESPONSE 6, 7	TRANSIENT	_	_	±400	_	_	±1500	-	_	±1500	mV pk
	RECOVERY	_	_	0.30	_	_	6	-	_	6	ms
STEP LINE RESPONSE ¹	Vin 16 to 40 VDC TRANSIENT	_	_	±800	_	_	±800	_	_	±800	mV pk
	RECOVERY	_	_	5	_	_	5	-	_	5	ms
START-UP	DELAY	_	_	6	_	_	6	_	_	6	ms
	OVERSHOOT ¹	_	_	500	_	_	2000	_	_	2000	mV pk

Notes

(All temperatures in tables and notes refer to case temperature, T_{C} .)

- 1. Guaranteed by design, not tested.
- 2. The sum of the two Aux outputs is not to exceed 10 watts. The maximum load per Aux output is 7.5 watts.
- 3. To maintain regulation when operating the ±Aux at full load, a minimum load of 300 mA is required on the main output. For Aux loads less than full load, a lower load (<300 mA) on the main output will maintain regulation.</p>
- $4. \, \mbox{Measured}$ on each output one at a time with the other outputs at full load.
- 5. Indefinite short circuit protection not guaranteed above 125°C (case).
- Response of each output as all outputs are simultaneously transitioned.
 Main: 50% 100% 50% of main full load
 Auxiliaries: 25% 50% 25% each, of total auxiliary full load
- 7. Recovery time is measured from application of the transient to point at which Vout is within 1% of regulation.
- 8. Tested on release from inhibit.

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Electrical Characteristics: -55 to +125° C T_C, 28 VDC Vin, 100% load, no irraditaion, unless otherwise specified.

TRIPLE OUTPUT MODEL -	- SMRT28515T	!	5 (MAIN	l)	+1	5 (+AL	IX)		15 (-AU)	()	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	25°C	4.95	5.00	5.05	14.77	15.00	15.23	-14.77	-15.00	-15.23	
		4.85	5.00	5.15	14.47	15.00	15.52	-14.47	-15.00	-15.52	VDC
OUTPUT CURRENT 2, 3		0.30	_	4.00	_	_	±0.50	_	_	±0.50	Α
OUTPUT POWER 2, 3		_	_	20	_	_	7.5	_	_	7.5	W
	TOTAL ALL OUTPUTS	_	_	30	-	_	_	_	_	_	•••
OUTPUT RIPPLE	10 кHz - 2 MHz	_	_	180	_	_	120	_	_	120	mV p-p
LINE REGULATION 4	Vin 16 to 40 VDC	_	-	20	_	_	75	_	_	75	mV
LOAD REGULATION 3, 4		_	_	50	_	_	75	_	_	75	mV
INPUT VOLTAGE	CONTINUOUS	16	28	40	_	-	-	_	-	_	VDC
	TRANSIENT 50 ms ¹	_	-	50	_	_	_	_	_	_	V
INPUT CURRENT	NO LOAD	_	-	110	_	_	_	_	_	_	mA
	INHIBITED	_	_	6	_	_	_	_	_	_	
INPUT RIPPLE CURRENT 3	10 kHz - 10 MHz	_	_	80	_	_	_	_	_	_	mA p-p
EFFICIENCY	25°C	72	73	_	_	_	_	_	_	_	%
		70	_	_	_	-	_	_	_	_	,•
LOAD FAULT ⁵	POWER DISSIPATION	_	-	14	_	-	-	_	-	1	W
ALL OUTPUTS SHORTED	RECOVERY EACH OUTPUT ¹	_	_	6	_	_	_	_	_	_	ms
STEP LOAD RESPONSE 6, 7	TRANSIENT	_	_	±400	_	_	±1500	_	_	±1500	mV pk
	RECOVERY	_	_	0.30	_	_	6	_	_	6	ms
STEP LINE RESPONSE ¹	VIN 16 TO 40 VDC TRANSIENT	_	_	±800	_	_	±800	_	_	±800	mV pk
	RECOVERY	_	_	5	_	_	5	_	_	5	ms
START-UP	DELAY	_	_	6	_	_	6	_	_	6	ms
	OVERSHOOT ¹	_	_	500	_	_	2000	_	_	2000	mV pk

Notes:

(All temperatures in tables and notes refer to case temperature, T_{C} .)

- 1. Guaranteed by design, not tested.
- The sum of the two Aux outputs is not to exceed 10 watts. The maximum load per Aux output is 7.5 watts.
- 3. To maintain regulation when operating the ±Aux at full load, a minimum load of 300 mA is required on the main output. For Aux loads less than full load, a lower load (<300 mA) on the main output will maintain regulation.
- 4. Measured on each output one at a time with the other outputs at full load.
- 5. Indefinite short circuit protection not guaranteed above 125°C (case).
- 6. Response of each output as all outputs are simultaneously transitioned. Main: 50% 100% 50% of main full load
- Auxiliaries: 25% 50% 25% each, of total auxiliary full load
- Recovery time is measured from application of the transient to point at which Vout is within 1% of regulation.
- 8. Tested on release from inhibit.

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Typical Performance Curves: 25°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.

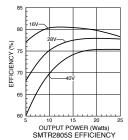


FIGURE 6

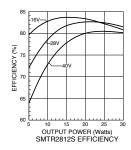


FIGURE 7

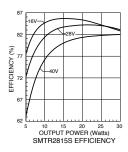


FIGURE 8

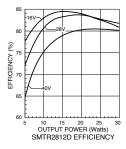


FIGURE 9

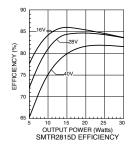


FIGURE 10

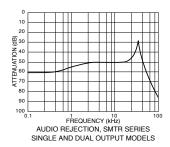


FIGURE 11

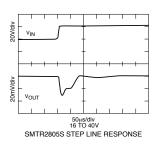


FIGURE 12

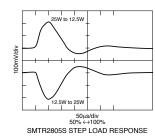


FIGURE 13

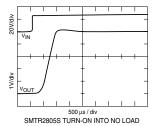
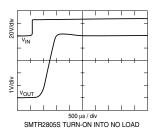
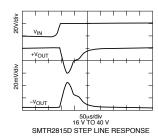


FIGURE 14

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Typical Performance Curves: 25°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.





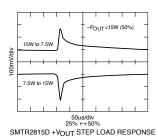
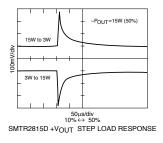
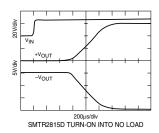


FIGURE 15

FIGURE 16

FIGURE 17 1





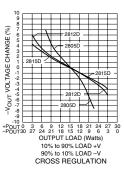
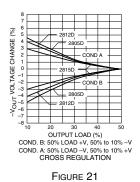


FIGURE 18 1

FIGURE 19

FIGURE 20



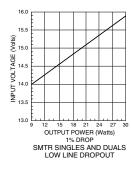


FIGURE 22

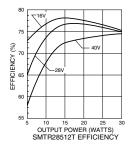


FIGURE 23

Notes

1. Percent (%) of power refers to the percent of the total output power of the converter.

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

Typical Performance Curves: 25°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.

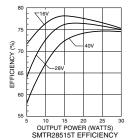


FIGURE 24

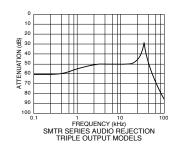


FIGURE 25

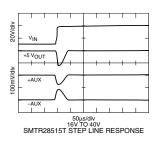


FIGURE 26

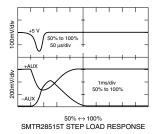


FIGURE 27

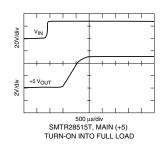


FIGURE 28

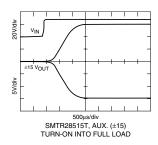


FIGURE 29

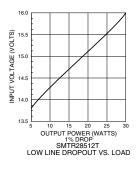


FIGURE 30

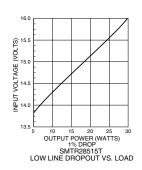
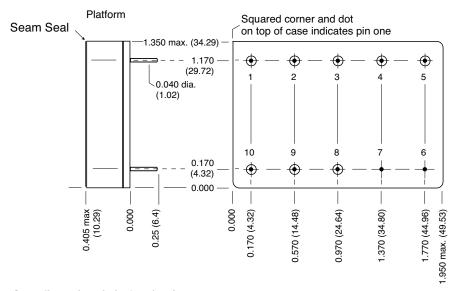


FIGURE 31

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28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

BOTTOM VIEW CASE F1



Case dimensions in inches (mm)

Tolerance ± 0.005 (0.13) for three decimal places ± 0.01 (0.3) for two decimal places unless otherwise specified

CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Case F1, Rev C, 20061211

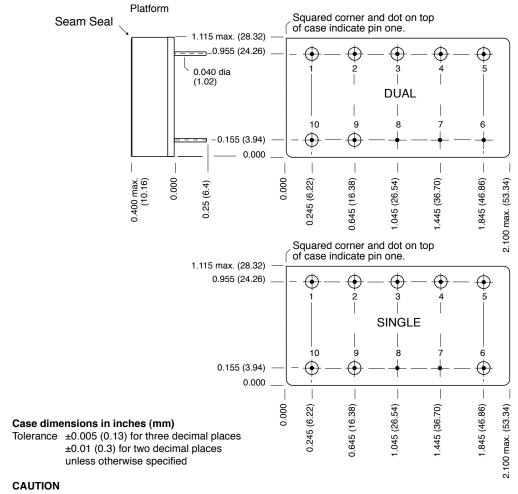
Please refer to the numerical dimensions for accuracy. All information is believed to be accurate, but no responsibility is assumed for errors or omissions. Interpoint reserves the right to make changes in products or specifications without notice.

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FIGURE 32: CASE F1 - TRIPLE OUTPUT MODELS

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

BOTTOM VIEW CASE H2



Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials

Header Cold Rolled Steel/Nickel/Gold

Kovar/Nickel Cover

Pins #52 alloy/Gold ceramic seal

Case H2, Rev D - 20090212

Please refer to the numerical dimensions for accuracy. All information is believed to be accurate, but no responsibility is assumed for errors or omissions. Interpoint reserves the right to make changes in products or specifications without notice.

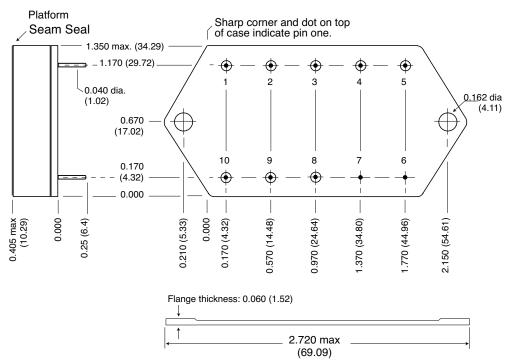
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FIGURE 33: CASE H2 - SINGLE & DUAL OUTPUT MODELS

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

BOTTOM VIEW CASE J1

Flanged cases: Designator "F" required in Case Option position of model number.



Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Case J1, Rev D, 20061211

Please refer to the numerical dimensions for accuracy. All information is believed to be accurate, but no responsibility is assumed for errors or omissions. Interpoint reserves the right to make changes in products or specifications without notice.

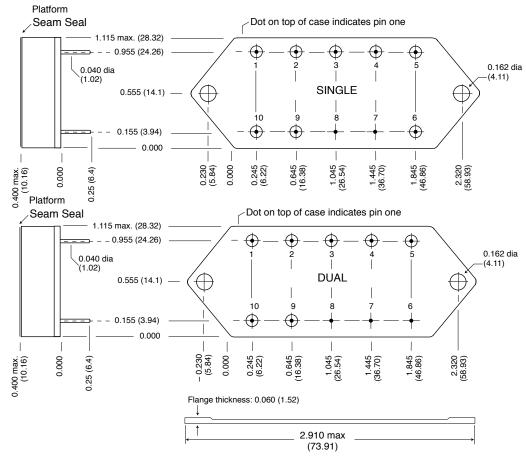
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FIGURE 34: CASE J1 - TRIPLE OUTPUT MODELS

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

BOTTOM VIEW CASE K3

Flanged cases: Designator "F" required in Case Option position of model number.



Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Case K3, Rev C, 20090212

Please refer to the numerical dimensions for accuracy.

All information is believed to be accurate, but no responsibility is assumed for errors or omissions.

Interpoint reserves the right to make changes in products or specifications without notice

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FIGURE 35: CASE K3 - SINGLE & DUAL OUTPUT MODELS

28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

CLASS H AND K, MIL-PRF-38534 ELEMENT EVALUATION

COMPONENT-LEVEL TEST PERFORMED		TOTYPE (O) QML ¹		ASS H ML	CLASS K QML		
	M/S ²	P^3	M/S ²	P^3	M/S ²	P^3	
Element Electrical	yes	no	yes	yes	yes	yes	
Element Visual	no	no	yes	yes	yes	yes	
Internal Visual	no	N/A	yes	N/A	yes	N/A	
Temperature Cycling	no	no	no	no	yes	yes	
Constant Acceleration	no	no	no	no	yes	yes	
Interim Electrical	no	N/A	no	N/A	yes	N/A	
Burn-in	no	N/A	no	N/A	yes	N/A	
Post Burn-in Electrical	no	N/A	no	N/A	yes	N/A	
Steady State Life	no	N/A	no	N/A	yes	N/A	
Voltage Conditioning Aging	N/A	no	N/A	no	N/A	yes	
Visual Inspection	no	no	N/A	no	N/A	yes	
Final Electrical	no	no	yes	yes	yes	yes	
Wire Bond Evaluation ⁴	no	no	yes	yes	yes	yes	
SEM	no	N/A	no	N/A	yes	N/A	
SLAM™/C-SAM: Input capacitors only (Add'I test, not req. by H or K)	no	no	no	yes	no	yes	

Notes

- Non-QML products do not meet all of the requirements of MIL-PRF-38534.
- 2. M/S = Active components (Microcircuit and Semiconductor Die)
- 3. P = Passive components
- 4. Not applicable to $\dot{\rm EMI}$ filters that have no wirebonds.

Definitions:

Element Evaluation: Component testing/screening per MIL-STD-883 as determined by MIL-PRF-38534

SEM: Scanning Electron Microscopy

SLAM™: Scanning Laser Acoustic Microscopy C-SAM: C - Mode Scanning Acoustic Microscopy

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28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

CLASS H AND K, MIL-PRF-38534 ENVIRONMENTAL SCREENING

END ITEM-LEVEL TEST PERFORMED	SPACE PROTOTYPE (O) NON-QML ¹	CLASS H QML	CLASS K QML
Non-destruct bond pull ² Method 2023	no	yes ³	yes
Pre-cap Inspection Method 2017, 2032	yes	yes	yes
Temperature Cycle (10 times) Method 1010, Cond. C, -65°C to 150°C, ambient	yes	yes	yes
Constant Acceleration Method 2001, 3000 g	yes	yes	yes
PIND Test Method 2020, Cond. A, 70 g @ 40 -200 Hz.	no	yes ³	yes
Pre burn-in test	yes	yes	yes
Burn-in Method 1015, 125°C case, typical 96 hours 160 hours 2 x 160 hours (includes mid-BI test)	yes no no	no yes no	no no yes
Final Electrical Test MIL-PRF-38534 Group A, Subgroups 1 through 6 -55°C, +25°C, +125°C case	yes	yes	yes
Radiography Method 2012	N/A	N/A	yes
Post Radiography Electrical Test Room temperature	N/A	N/A	yes ³
Hermeticity Test Fine Leak, Method 1014, Cond. A Gross Leak, Method 1014, Cond. C	yes yes	yes yes	yes yes
Final visual inspection Method 2009	yes	yes	yes

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

- Space Prototype (O), non-QML products, do not meet all of the requirements of MIL-PRF-38534.
 Not applicable to EMI filters that have no wirebonds.
- 3. Not required by DSCC but performed to assure product quality.

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28 VOLT INPUT - 30 WATT - PENDING REVISION F RELEASE

CLASS H AND K, MIL-PRF-38534 RADIATION ASSURANCE

ENIVIDONINAENITAI	CODEENING LEVEL O
ENVIRUNMENTAL	SCREENING LEVELS

RADIATION HARDNESS ASSURANCE LEVELS	SPACE PROTOTYPE (O) NON-QML ²	CLASS H QML	CLASS K
O ¹ : Standard, no radiation guarantee	00	N/A	N/A
P ³ : Radiation tolerant–Tested lots up to 30 K Rads (Si) total dose SEU guarantee up to 40 MeV	N/A	HP	KP
R ³ : Radiation tolerant–Tested lots up to 100 K Rads (Si) total dose SEU guarantee up to 40 MeV	N/A	HR	KR

Notes:



^{1.} Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA".

^{2.} Space Prototype (O), non-QML, products do not meet all of the requirements of MIL-PRF-38534.

^{3.} Redmond site, Interpoint, has a Radiation Hardness assurance plan on file with DSCC.